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Docket No.: GR 00 P 1807

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : THIEMO LANG

Filed : Concurrently herewith

Title : DISPERSION COMPENSATOR AND METHOD
OF COMPENSATING FOR DISPERSION

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Koichi Takiguchi et al.: "Planar Lightwave Circuit Dispersion Equalizer", Journal of Lightwave Technology, Vol. 14, No. 9, dated September 1966, pages 2003 – 2011 (as mentioned on page 5 of the Specification);

Koichi Takiguchi et al.: "Dispersion Slope Equalizer for Dispersion Shiften Fiber Using a Lattice-Form Programmable Optical Filter on a Planar Lightwave Circuit", Journal of Lightwave Technology, Vol. 16, No. 9, September 1998, pages 1647 – 1656 (as mentioned on page 5 of the Specification);

Kaname Jinguji et al.: "Synthesis of Coherent Two-Port Lattice-Form Optical Delay-Line Circuit", Journal of Lightwave Technology, Vol. 13, No. 1, January 1995, pages 73 – 82 (as mentioned on page 6 of the Specification);

C.K. Madsen et al.: "Optical All-Pass Filters for Phase Response Design with Applications for Dispersion Compensation", IEEE Photonics Technology Letters, Vol. 10, No. 7, July 1998, pages 994 – 996 (as mentioned on page 6 of the Specification)

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicant

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Date: April 30, 2001

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